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1 [Implementation aspects of a SPARC V9 complete machine simulator](#)

Bill Clarke, Adam Czezowski, Peter Strazdins

 January 2002 **Australian Computer Science Communications , Proceedings of the twenty-fifth Australasian conference on Computer science - Volume 4**,
 Volume 24 Issue 1
Full text available: [pdf\(1.33 MB\)](#)
 Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

In this paper we present work in progress in the development of a complete machine simulator for the UltraSPARC, an implementation of the SPARC V9 architecture. The complexity of the UltraSPARC ISA presents many challenges in developing a reliable and yet reasonably efficient implementation of such a simulator. Our implementation includes a heavily object-oriented design for the simulator modules and infrastructure, caching of repeated computations for performance, adding an OS (system call) emu ...

Keywords: SMP, SPARC V9 ISA, UltraSPARC, complete machine simulator, execution-driven simulation, object-oriented design

2 [Compiling nested data-parallel programs for shared-memory multiprocessors](#)

Siddhartha Chatterjee

 July 1993 **ACM Transactions on Programming Languages and Systems (TOPLAS)**,
 Volume 15 Issue 3
Full text available: [pdf\(4.17 MB\)](#)
 Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#), [review](#)

Keywords: compilers, data parallelism, shared-memory multiprocessors

3 [Lightweight remote procedure call](#)

Brian N. Bershad, Thomas E. Anderson, Edward D. Lazowska, Henry M. Levy

 February 1990 **ACM Transactions on Computer Systems (TOCS)**, Volume 8 Issue 1
Full text available: [pdf\(1.60 MB\)](#)
 Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

Lightweight Remote Procedure Call (LRPC) is a communication facility designed and optimized for communication between protection domains on the same machine. In

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